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NIXON PEABODY

NO. 2365 P. 1

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**FAX**

**Date:** May 5, 2004 **Pages (including cover):** 3  
**To:** USPTO **Fax:** 703-746-9195 **Ph:**  
**From:** Donald R. Studebaker  
**Message:** Dear Sir/Madam,

Please send a corrected filing receipt for the attached application (Serial No. 10/663,977). The title is spelled incorrectly. Please delete "Interfact circuit" and insert - - Interface circuit - -. Thank you.

Sincerely,



Donald R. Studebaker

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**Date:** May 5, 2004 **Pages (including cover):** 3  
**To:** USPTO **Fax:** 703-746-9195 **Ph:**  
**From:** Donald R. Studebaker  
**Client/Matter:** 031794/3 **User No.:** 8695 **Disbursement Amount:** \$

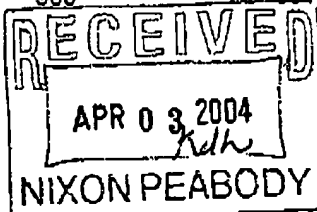


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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/663,977	09/17/2003	2181	900	031794-3	7	8	2

22204  
NIXON PEABODY, LLP  
401 9TH STREET, NW  
SUITE 900  
WASHINGTON, DC 20004-2128



**CONFIRMATION NO. 1949**  
**UPDATED FILING RECEIPT**

\*00000000012256405\*

Date Mailed: 04/01/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

**Applicant(s)**

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### Assignment For Published Patent Application

**Oki Electric Industry Co., Ltd., Tokyo, JAPAN;**

**Domestic Priority data as claimed by applicant**

## Foreign Applications

JAPAN 2002-275787 09/20/2002

**If Required, Foreign Filing License Granted: 12/10/2003**

**Projected Publication Date: 07/08/2004**

**Non-Publication Request: No**

**Early Publication Request: No**

Title

Interface circuit

INTERFACE CIRCUIT

Preliminary Class

710

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Title 37, Code of Federal Regulations, 5.11 & 5.15**

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